



ENABLING ARTIFICIAL INTELLIGENCE WITH ENGINEERED SUBSTRATES

January 2024

DISCLAIMER

The Company's business operations and financial position are described in the Company's 2022-2023 Universal Registration Document (which notably includes the 2022-2023 Annual Financial Report) which was filed on June 14, 2023 with the French stock market authority (Autorité des Marchés Financiers, or AMF) under number D.23-0482 as well as in the Company's 2023-2024 half-year report released on November 15, 2023. The French versions of the 2022-2023 Universal Registration Document and of the 2023-2024 half-year report, together with English courtesy translation for information purposes of both documents, are available for consultation on the Company's website (www.soitec.com), in the section Company - Investors - Financial Reports

Your attention is drawn to the risk factors described in Chapter 2.1 (Risk factors and controls mechanism) of the Company's 2022-2023 Universal Registration Document.

This document contains summary information and should be read in conjunction with the 2022-2023 Universal Registration Document and the 2023-2024 half-year report.

This document contains certain forward-looking statements. These forward-looking statements relate to the Company's future prospects, developments and strategy and are based on analyses of earnings forecasts and estimates of amounts not yet determinable. By their nature, forward-looking statements are subject to a variety of risks and uncertainties as they relate to future events and are dependent on circumstances that may or may not materialize in the future. Forward-looking statements are not a guarantee of the Company's future performance. The occurrence of any of the risks described in Chapter 2.1 (Risk factors and controls mechanism) of the 2022-2023 Universal Registration Document may have an impact on these forward-looking statements. In particular, the future consequences of geopolitical conflicts, notably the Ukraine / Russia situation, as well as rising inflation, may result in greater impacts than currently anticipated in these forward-looking statements.

The Company's actual financial position, results and cash flows, as well as the trends in the sector in which the Company operates may differ materially from those contained in this document. Furthermore, even if the Company's financial position, results, cash-flows and the developments in the sector in which the Company operates were to conform to the forward-looking statements contained in this document, such elements cannot be construed as a reliable indication of the Company's future results or developments.

The Company does not undertake any obligation to update or make any correction to any forward-looking statement in order to reflect an event or circumstance that may occur after the date of this document. In addition, the occurrence of any of the risks described in Chapter 2.1 (Risk factors and controls mechanism) of the 2022-2023 Universal Registration Document may have an impact on these forward-looking statements.

This document does not constitute or form part of an offer or a solicitation to purchase, subscribe for, or sell the Company's securities in any country whatsoever. This document, or any part thereof, shall not form the basis of, or be relied upon in connection with, any contract, commitment or investment decision.

Notably, this document does not constitute an offer or solicitation to purchase, subscribe for or to sell securities in the United States. Securities may not be offered or sold in the United States absent registration or an exemption from the registration under the U.S. Securities Act of 1933, as amended (the "Securities Act"). The Company's shares have not been and will not be registered under the Securities Act. Neither the Company nor any other person intends to conduct a public offering of the Company's securities in the United States.

AGENDA

#01

ARTIFICIAL INTELLIGENCE
FUNDAMENTALS

#02

ARTIFICIAL INTELLIGENCE ADOPTION
ACROSS SOITEC END MARKETS

#03

SOITEC ENGINEERED SUBSTRATES TO ENABLE
ARTIFICIAL INTELLIGENCE ADOPTION

AI

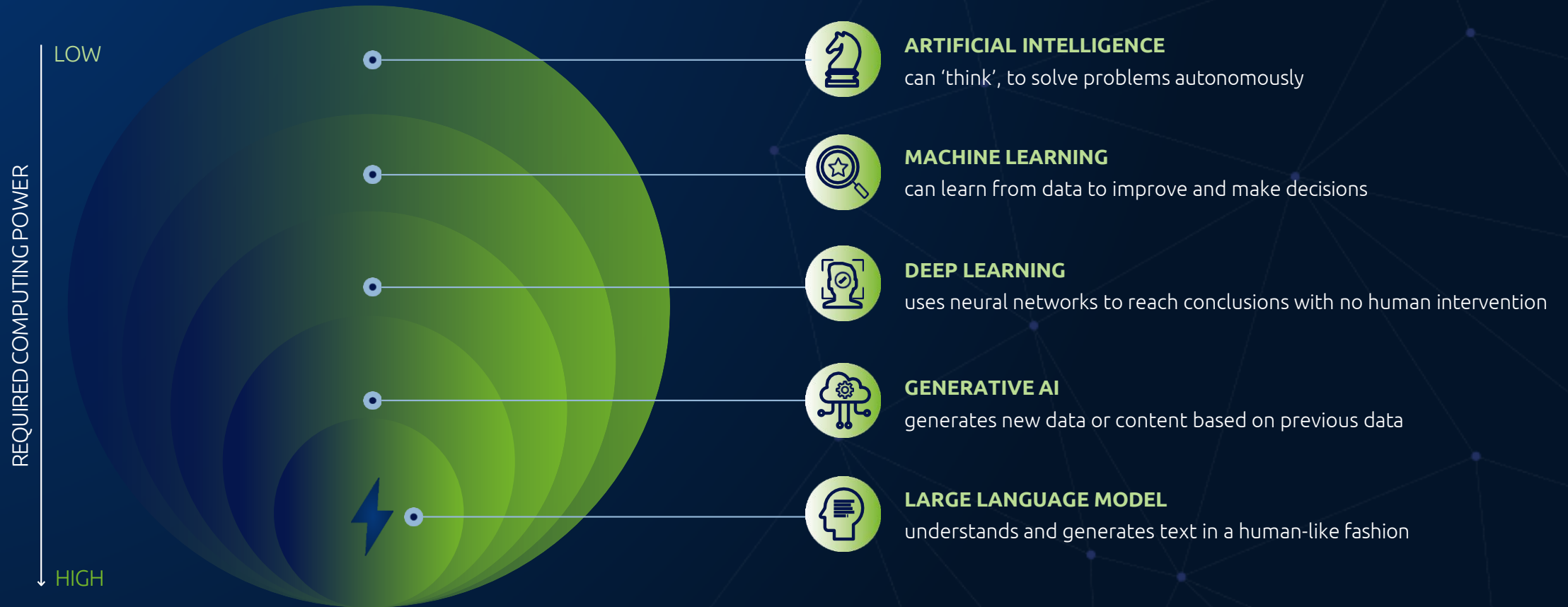
FUNDAMENTALS



“Artificial intelligence is a machine’s ability to perform the cognitive functions we usually associate with human minds”

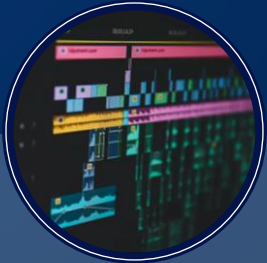
Source: McKinsey

WHAT IS ARTIFICIAL INTELLIGENCE?



Source: Accenture, SCS

ARTIFICIAL INTELLIGENCE IS TRANSFORMING OUR DAILY LIVES



Content Creation

Offering new tools for content creators



Virtual Assistants

Augmenting productivity in everyday tasks



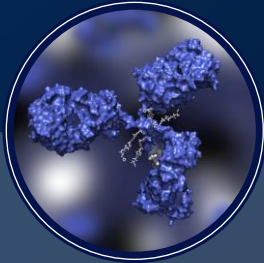
Climate Research

Helping to combat climate change



Smart Mobility

Driving Automotive Autonomy & Efficiency



Healthcare & Lifesciences

Discovering new drugs & preventive treatments



Wearables & Hearables

Delivering personal aid to overcome disabilities



Security & Privacy

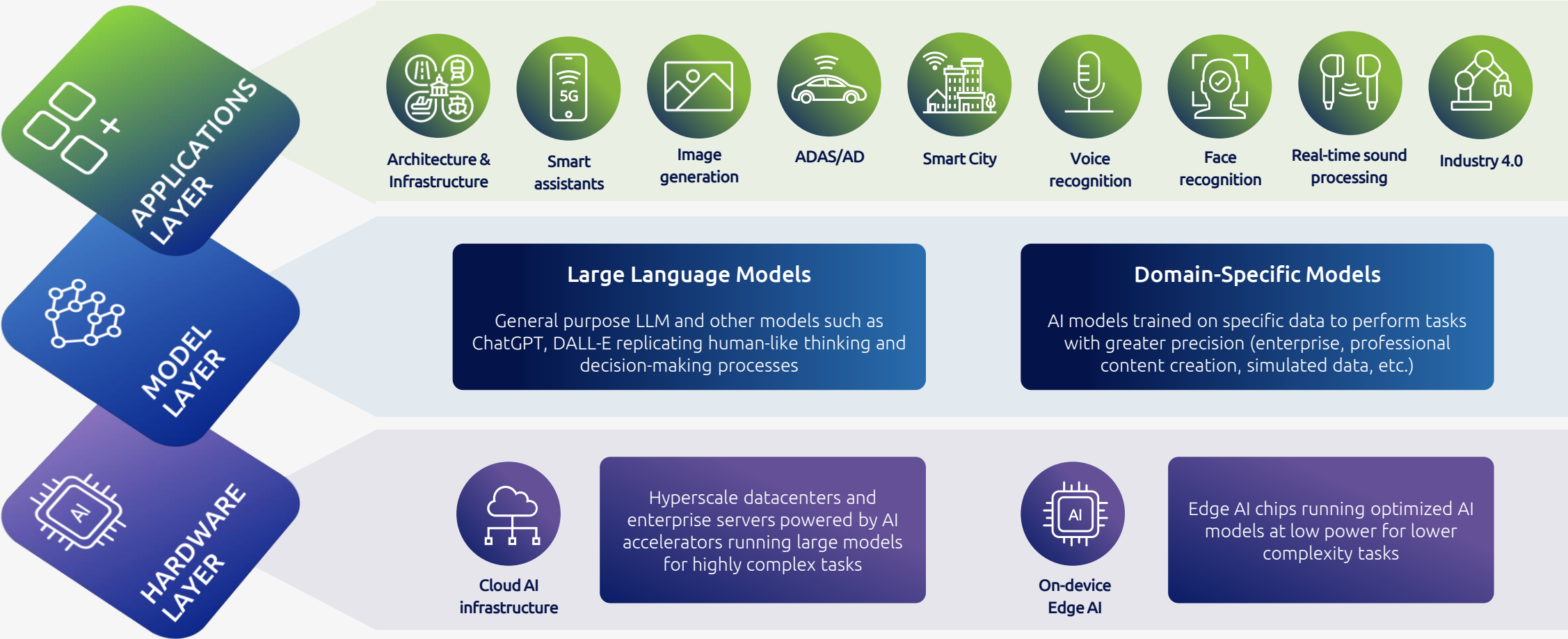
Enhancing threat detection & prevention



Industry 4.0

Accelerating automation & efficiency roadmaps

WHAT IS THE INFRASTRUCTURE ENABLING ARTIFICIAL INTELLIGENCE?



Source: Qualcomm, Red Hat

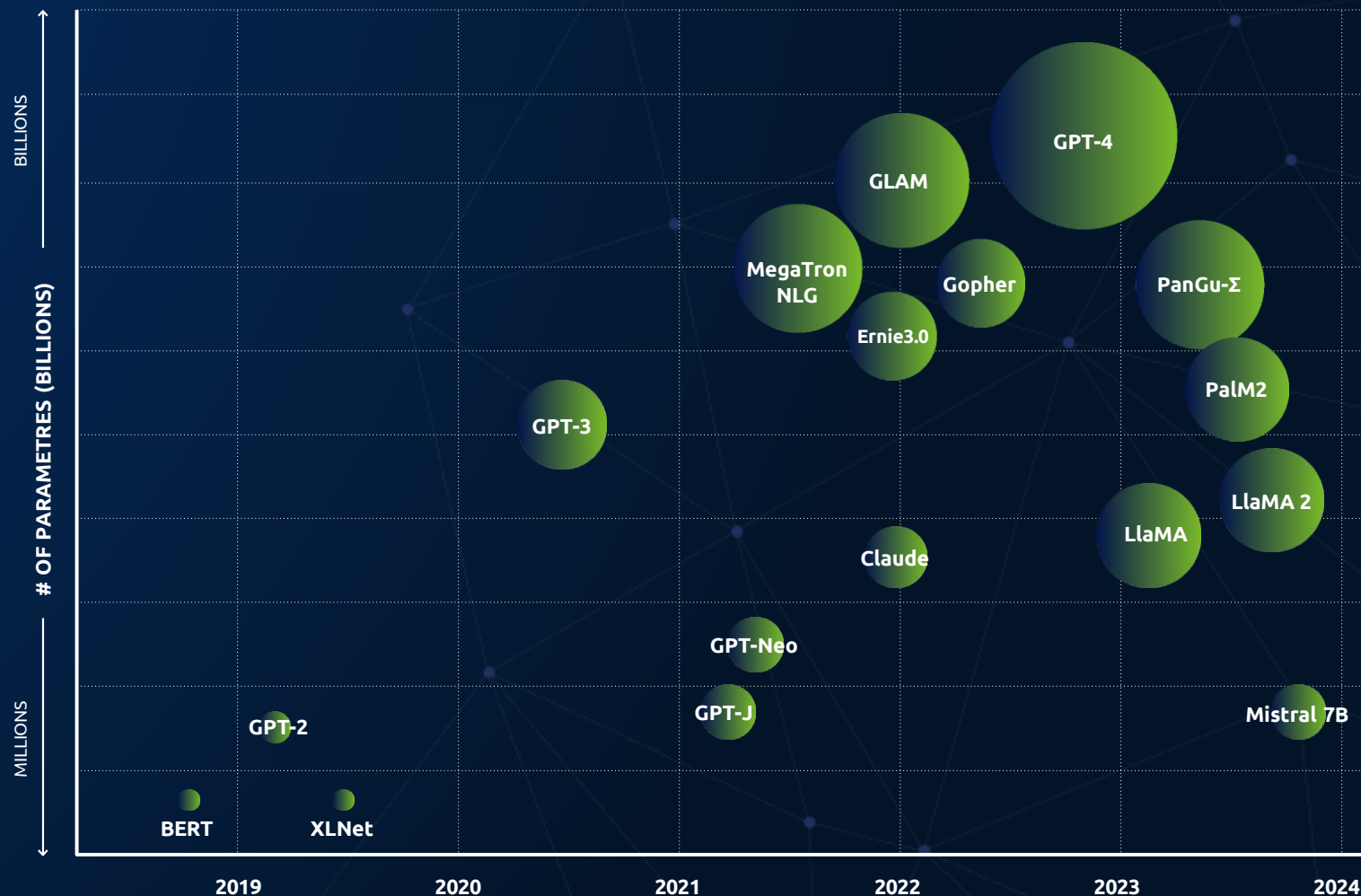




LARGE LANGUAGE MODELS TIMELINE & SIZE

EXPONENTIAL
GROWTH OF LLM
PARAMETERS ENABLES
BREAKTHROUGH AI
APPLICATIONS

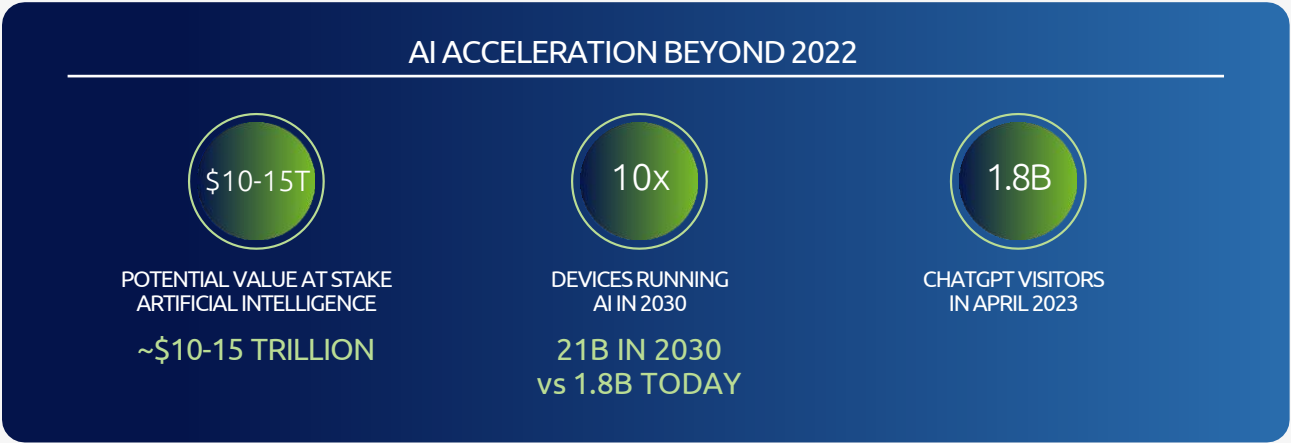
 = 50B parameters



Source: publicly available data

ARTIFICIAL INTELLIGENCE

EXPONENTIAL GROWTH IN COMPUTING POWER



Healthcare diagnostic



Autonomous driving



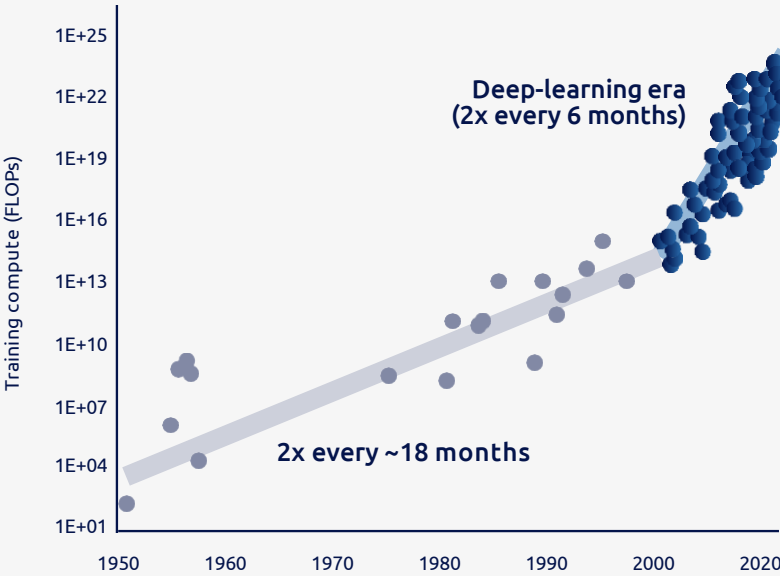
Industry 4.0



Digital creation

Source: McKinsey 2022, Transforma Insights, OpenAI

Acceleration of Computing Power Beyond Moore's Law

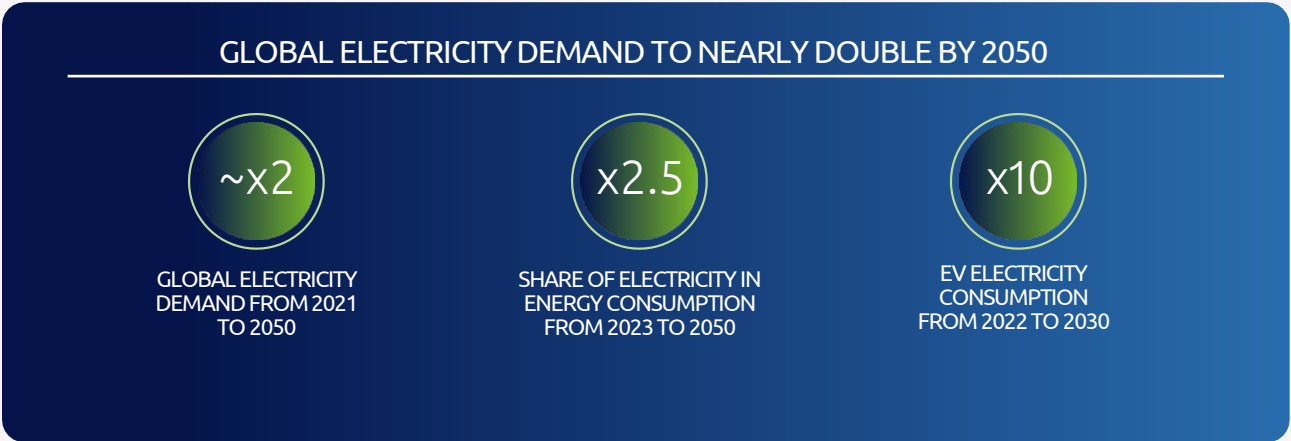


Source: Cornell University (Sevilla et al)



ENERGY EFFICIENCY SOLUTIONS

MANDATORY TO SATISFY GLOBAL ELECTRICITY DEMAND



Industrial applications



Datacenters



Smart Cities



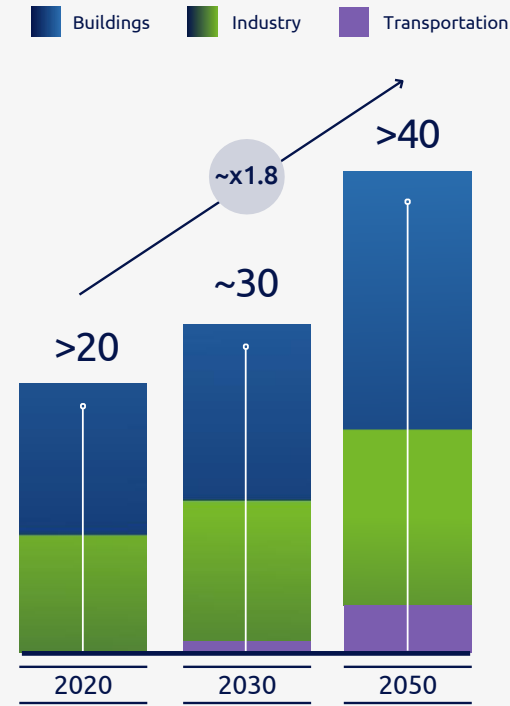
EV charging infrastructure

Source: IEA World Energy Outlook 2022

(1) The Stated Policies Scenario reflects existing policies and measures, as well as firm policy ambitions and objectives that have been legislated by governments around the world

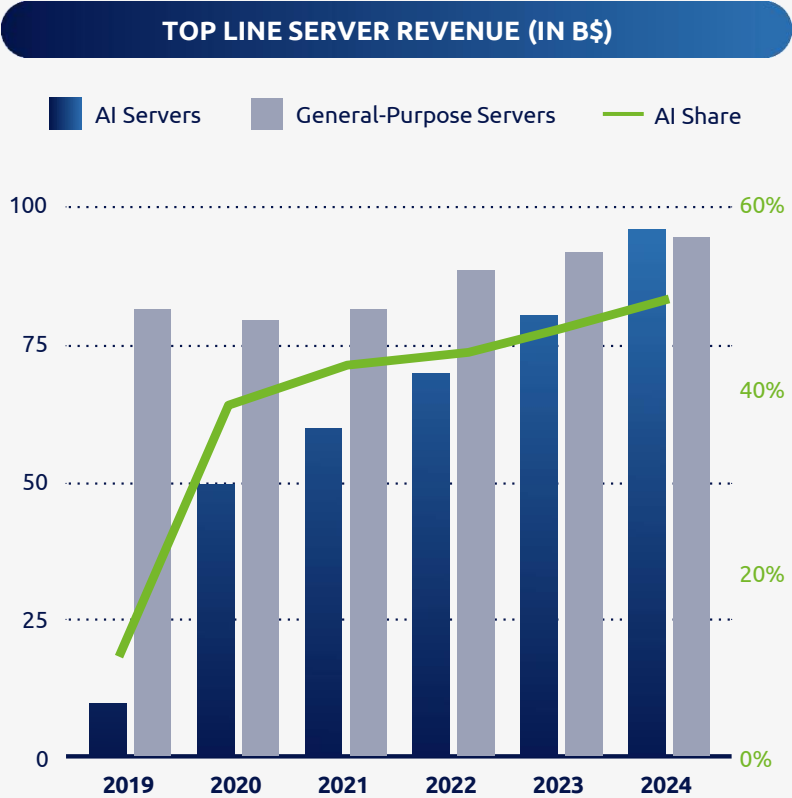
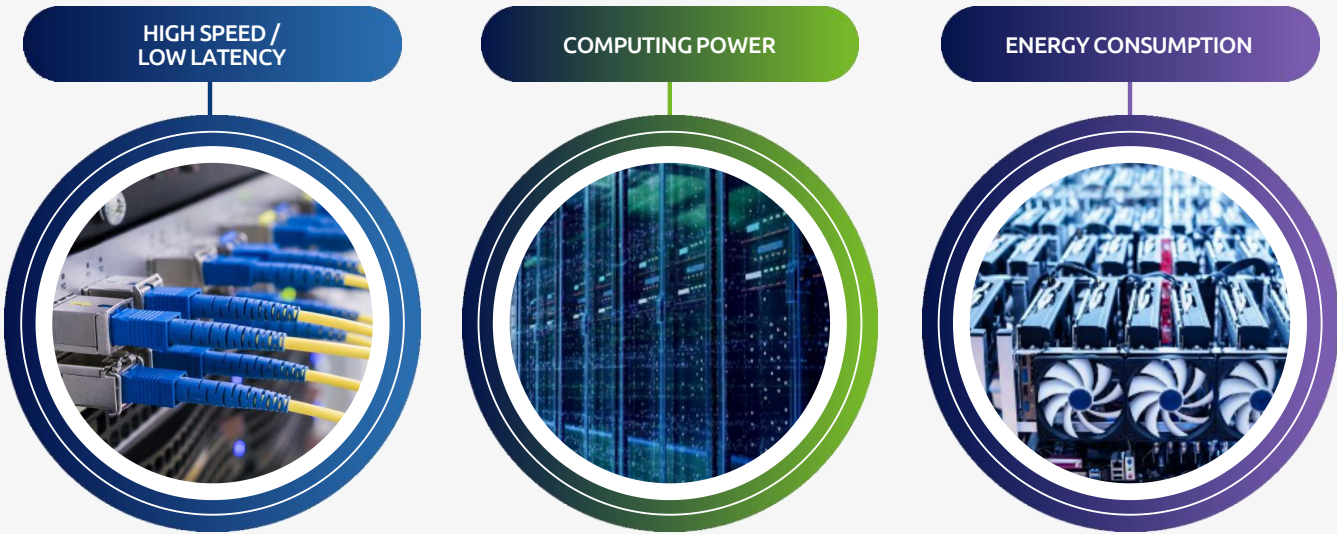
Global electricity demand requires energy saving solutions

ELECTRICITY DEMAND IN TWH (STEPS' IEA SCENARIO)



DATA CENTRES

AI SERVER REVENUE SET TO OVERTAKE GENERAL-PURPOSE SERVERS

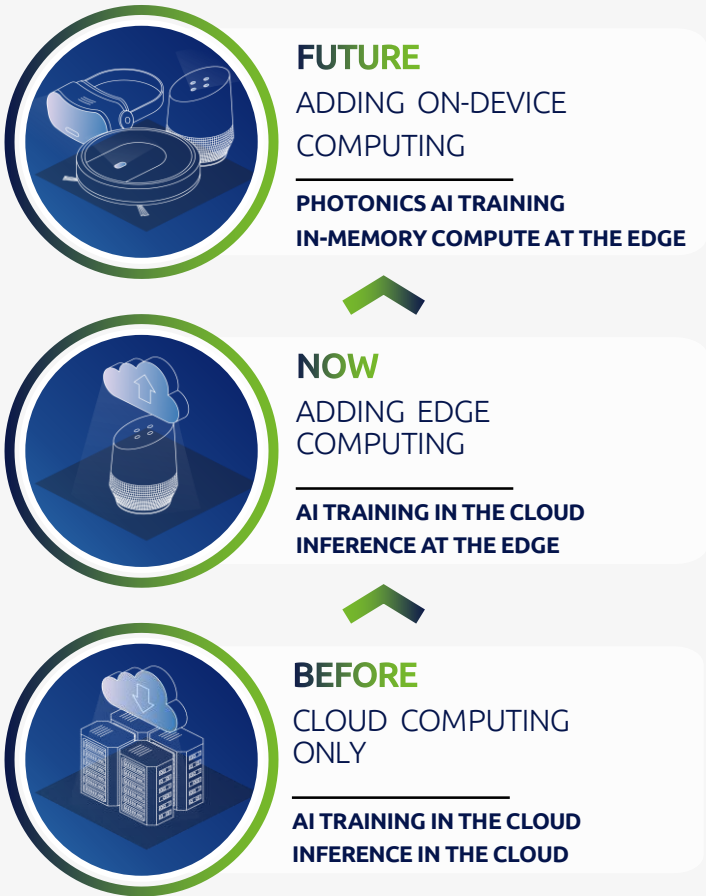


Source: IDC, Applied Materials 2021, NVIDIA, Yole server processors forecast, Soitec estimates

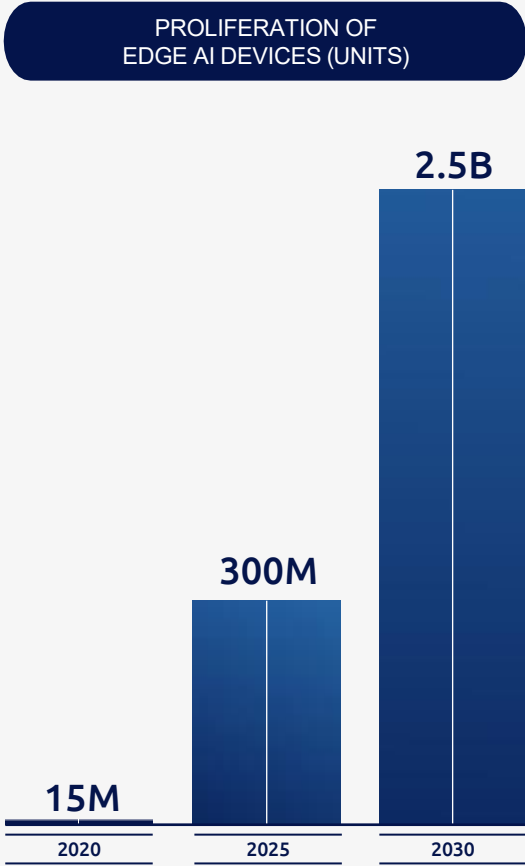
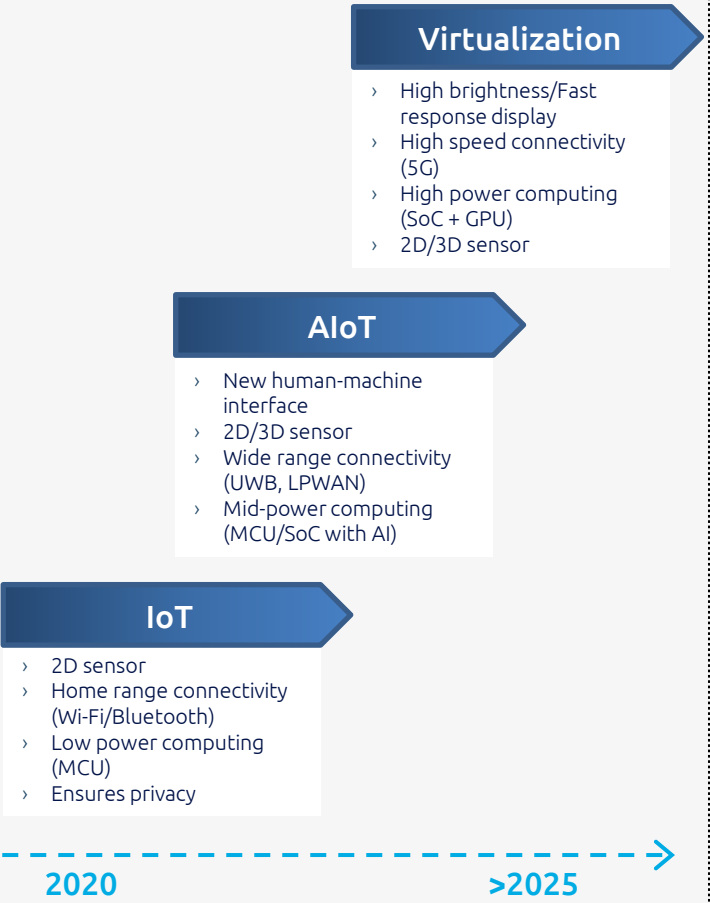


ARTIFICIAL INTELLIGENCE

WHY EDGE COMPUTING



INTELLIGENCE AT THE EDGE



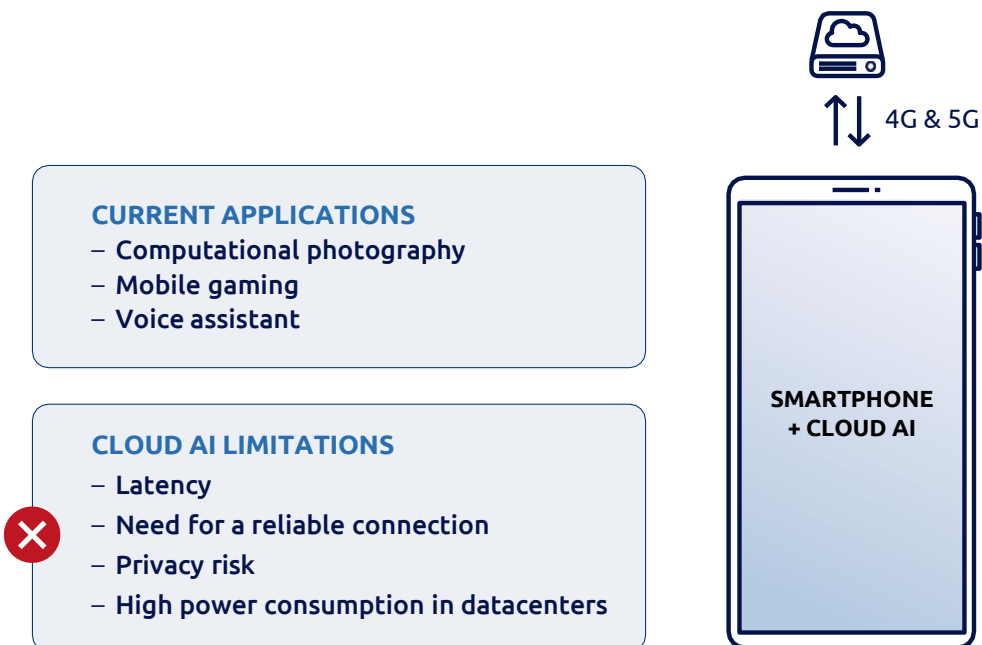
Source: ABI research 2021



AI ADOPTION ACROSS SOITEC END MARKETS

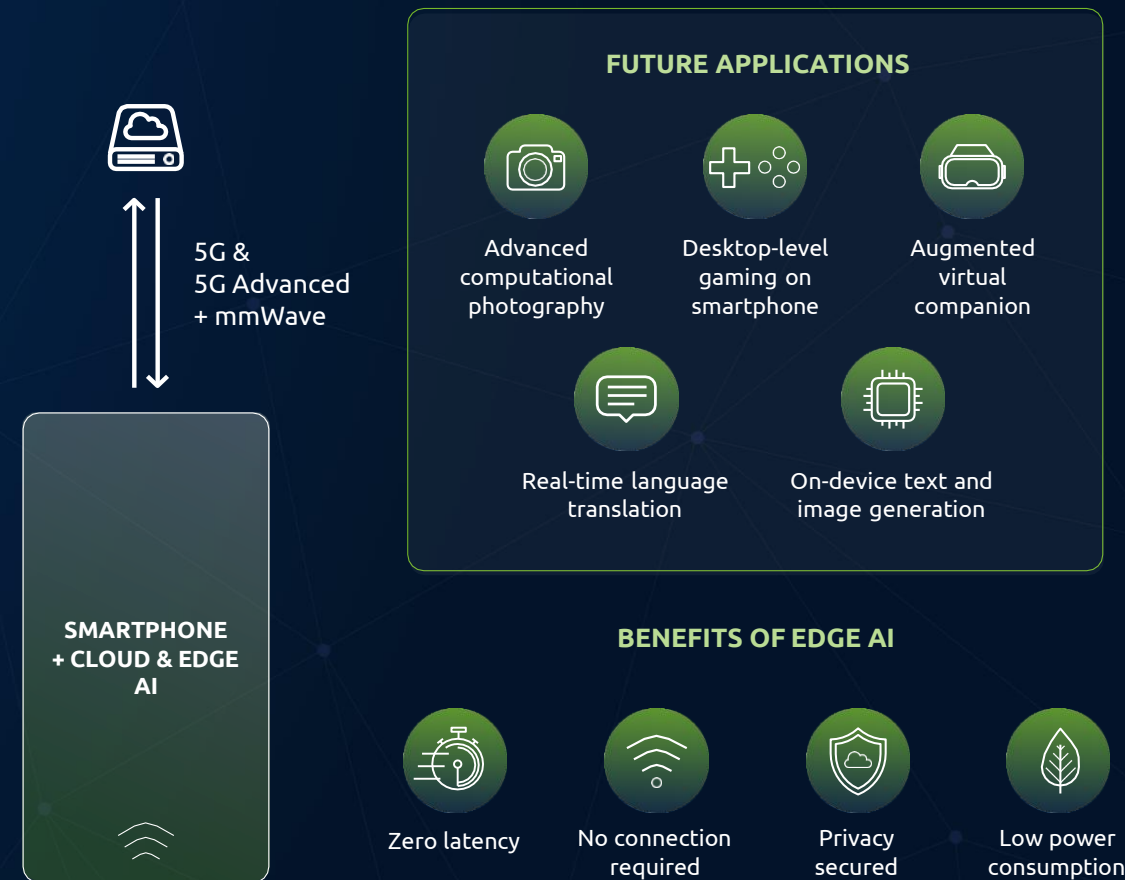
MOBILE COMMUNICATIONS

EDGE AI SMARTPHONES



TODAY

Source: Qualcomm



TOMORROW



AUTOMOTIVE & INDUSTRIAL

AI TRANSFORMING THE AUTOMOTIVE BEYOND AUTONOMOUS DRIVING

ADVANCED DRIVER ASSISTANCE & AUTONOMOUS DRIVING SYSTEMS (ADAS/AD)



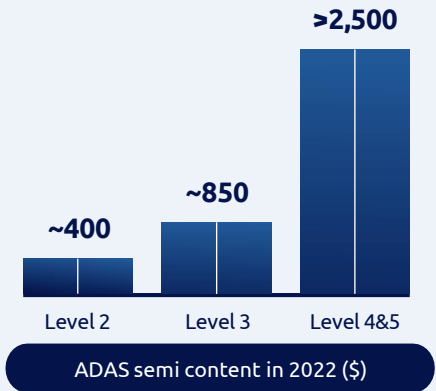
Advancing automation features from ADAS to AD and improving functional safety

AI APPLICATIONS

- Object recognition
- Pedestrian detection
- Traffic sign detection
- Lane-crossing detection
- Speed limitation

DATA COLLECTION, COMMUNICATION, COMPUTATION

- Front, Rear, Edge & imaging radars
- MCU / MPU
- LiDARs
- Zonal Edge computing
- Airbag / Braking system



ENHANCED IN-VEHICLE EXPERIENCE



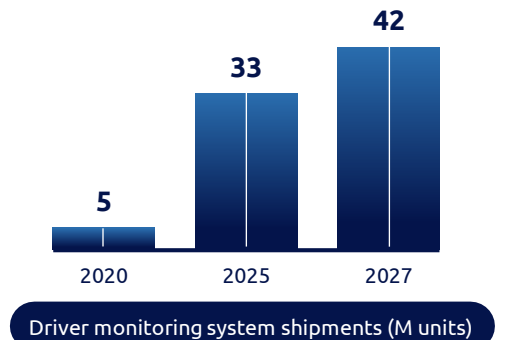
The car increasingly becoming a connected hub with in-cabin sensing and computing

AI APPLICATIONS

- Eye-tracking
- Behavior monitoring
- Voice recognition
- Virtual assistance

DATA COLLECTION, COMMUNICATION, COMPUTATION

- In-vehicle Sensors & Actuators
- Cameras
- Vehicle Networking
- Multimedia application processor
- Class D audio amplifier



Source: Yole, UBS



AUTOMOTIVE & INDUSTRIAL ACCELERATING JOURNEY TO INDUSTRY 4.0

AI POWERED MANUFACTURING WITH EDGE AI SOLUTIONS

Improving product quality and yield with intelligent, secure and adaptable manufacturing operations

30%

YIELD IMPROVEMENT

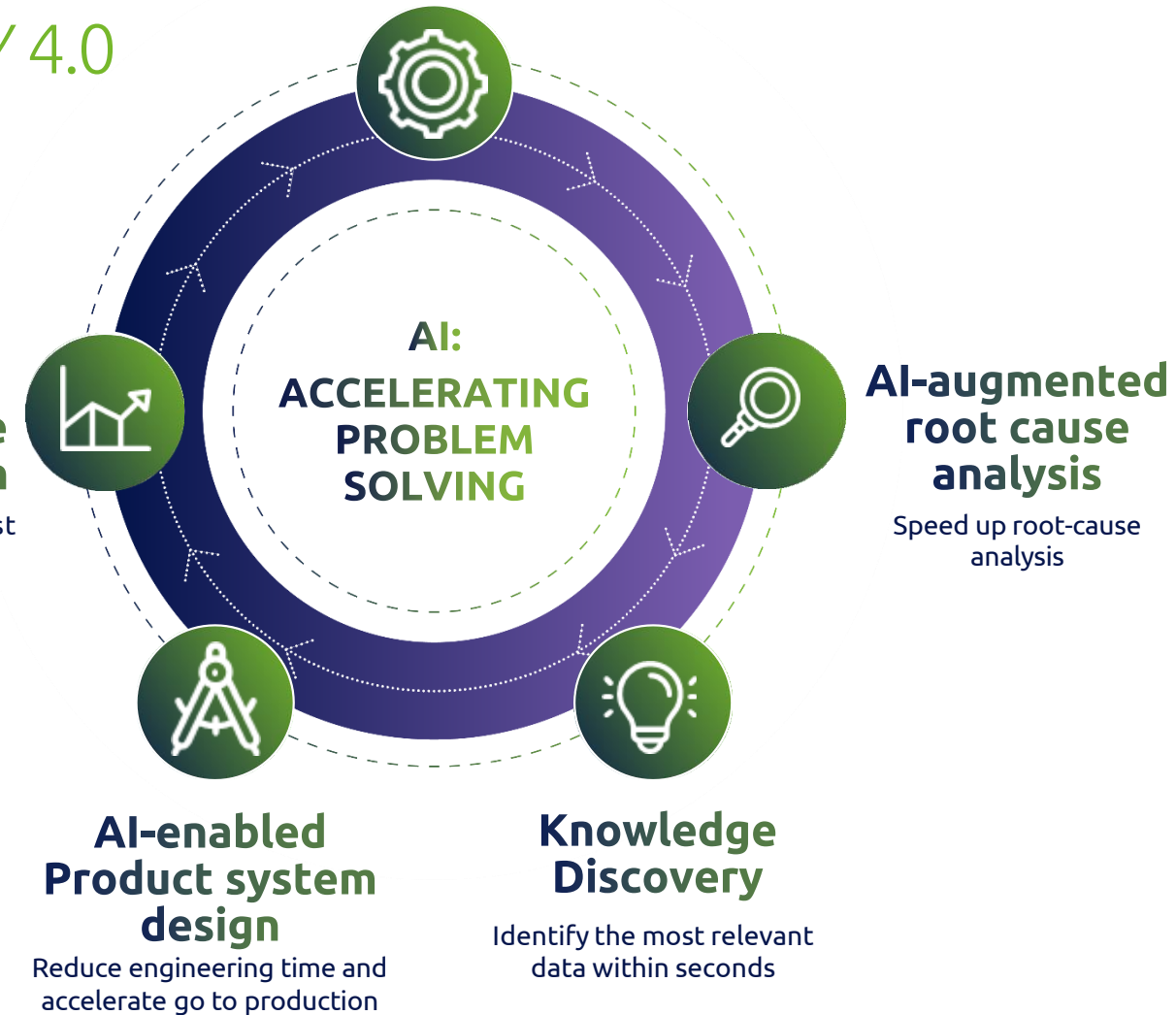
15%

WASTE REDUCTION

10%

LOWER OPERATING COSTS

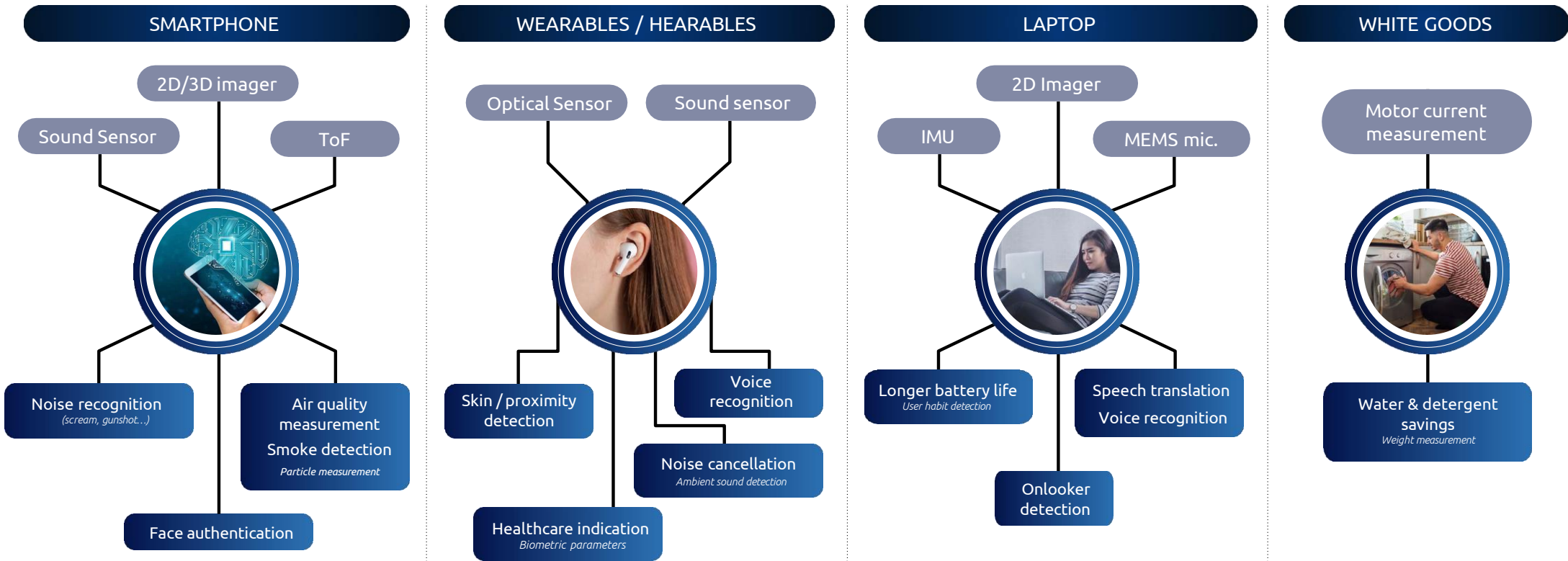
Source: McKinsey, IBM





SMART DEVICES

FROM THE EDGE TO THE ENDPOINT, AI ENHANCING MULTIPLE USE CASES



AI AT THE EDGE ENABLING

Lower latency

Power saving

Enhanced user experience

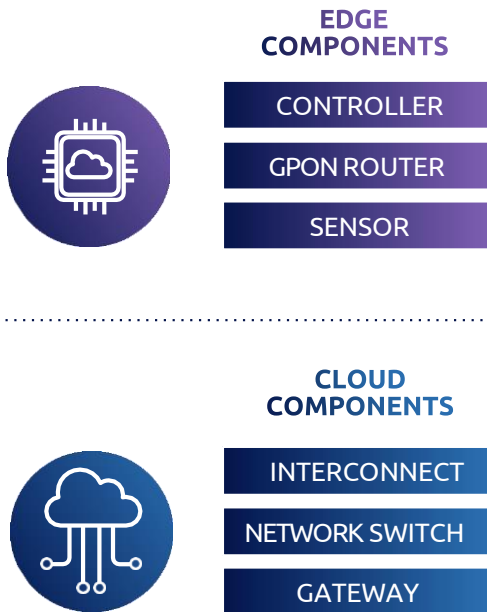
Greater privacy

Source: STMicroelectronics, Soitec internal data

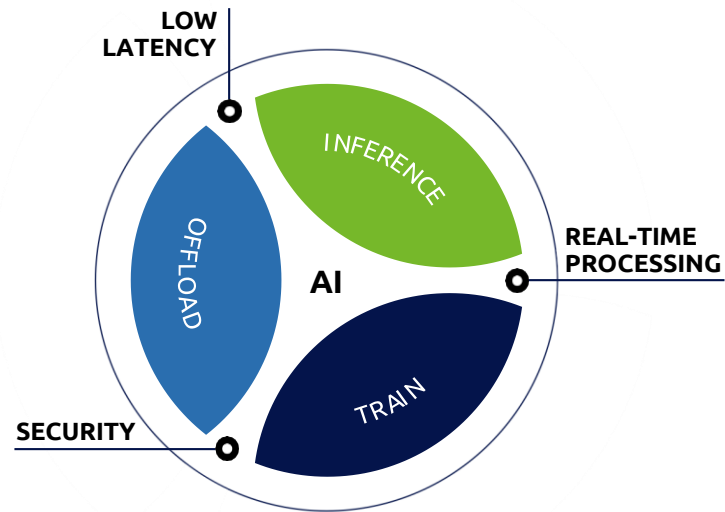


SMART DEVICES BRIDGING CLOUD TO THE EDGE

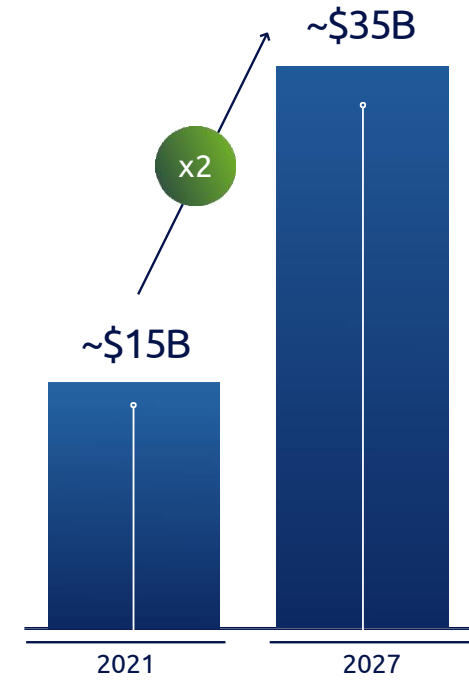
SOITEC COMPUTING



WHERE SOITEC MEETS AI



EDGE AI CHIPSET MARKET SET TO DOUBLE THROUGH 2027

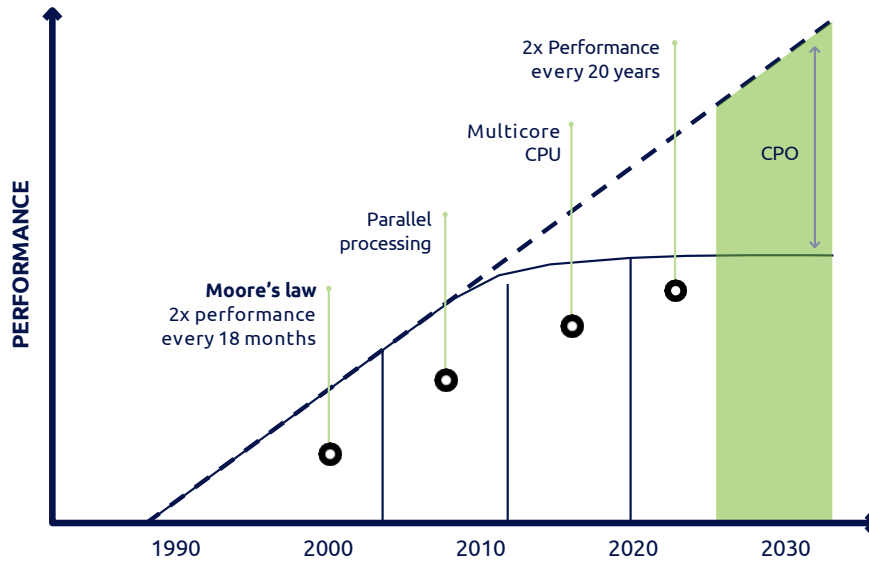


Source: ABI 2022



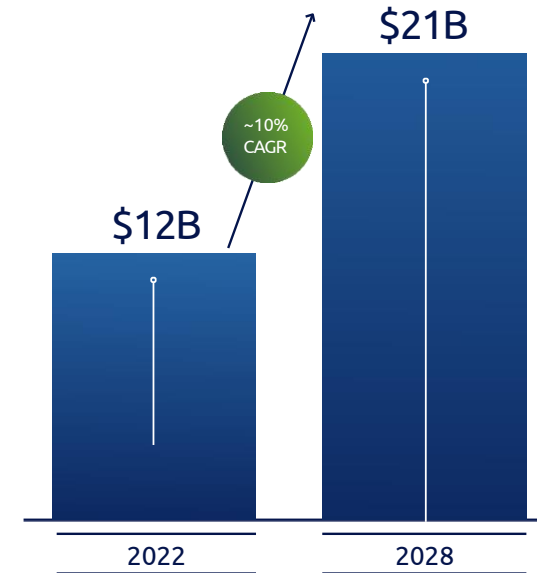
SMART DEVICES CLOUD INFRASTRUCTURE

CO-PACKAGED OPTICS (CPO) AS A MEANS TO
boost Cloud AI Connectivity



Source: Broadcom

OPTICAL TRANSCEIVER MARKET
GROWING ~10% CAGR OVER 2022-2028



Source: Yole



SMART DEVICES

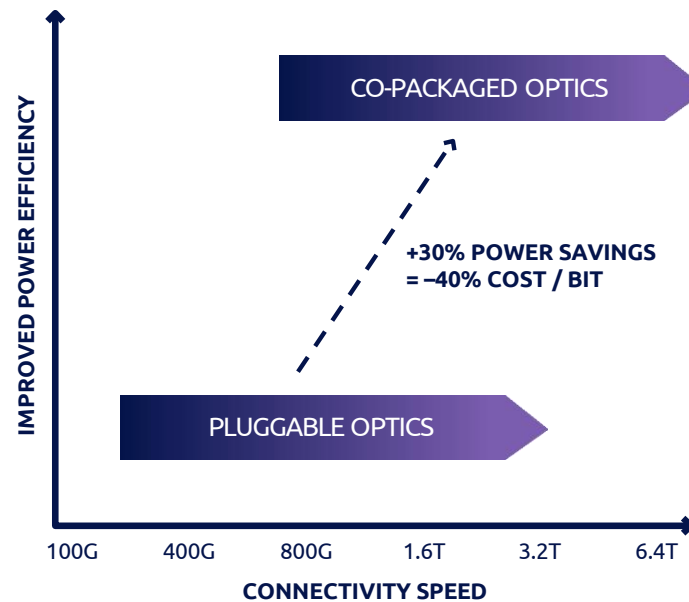
AI / ML ENABLEMENT AS A NETWORK SOLUTION

CURRENT AI / ML INFRASTRUCTURE IS
BANDWIDTH x DISTANCE LIMITED

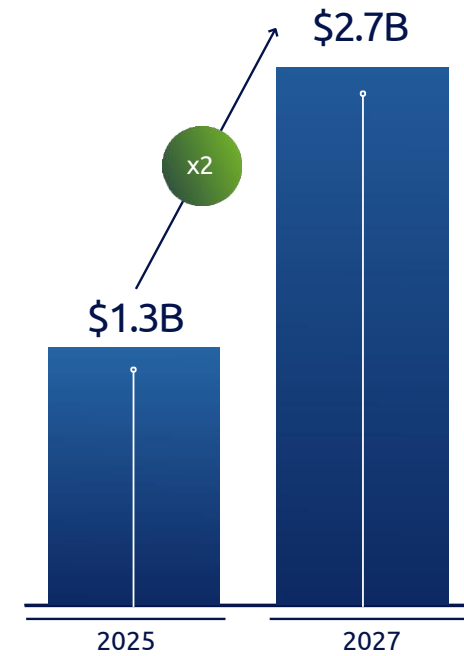
	TODAY	WITHIN DECADE
Data Center Interconnect (DCI)	OPTICAL	
Rack-to-Rack	OPTICAL	
Board-to-Board		OPTICAL
Chip-to-Module	ELECTRICAL	OPTICAL
Chip-to-Chip	ELECTRICAL	
AI / ML		

Source: Yole & Soitec

FUTURE AI / ML INFRASTRUCTURE AS
A MULTI-LAYERED NETWORK



CPO MARKET TO DOUBLE
OVER 2025-2027

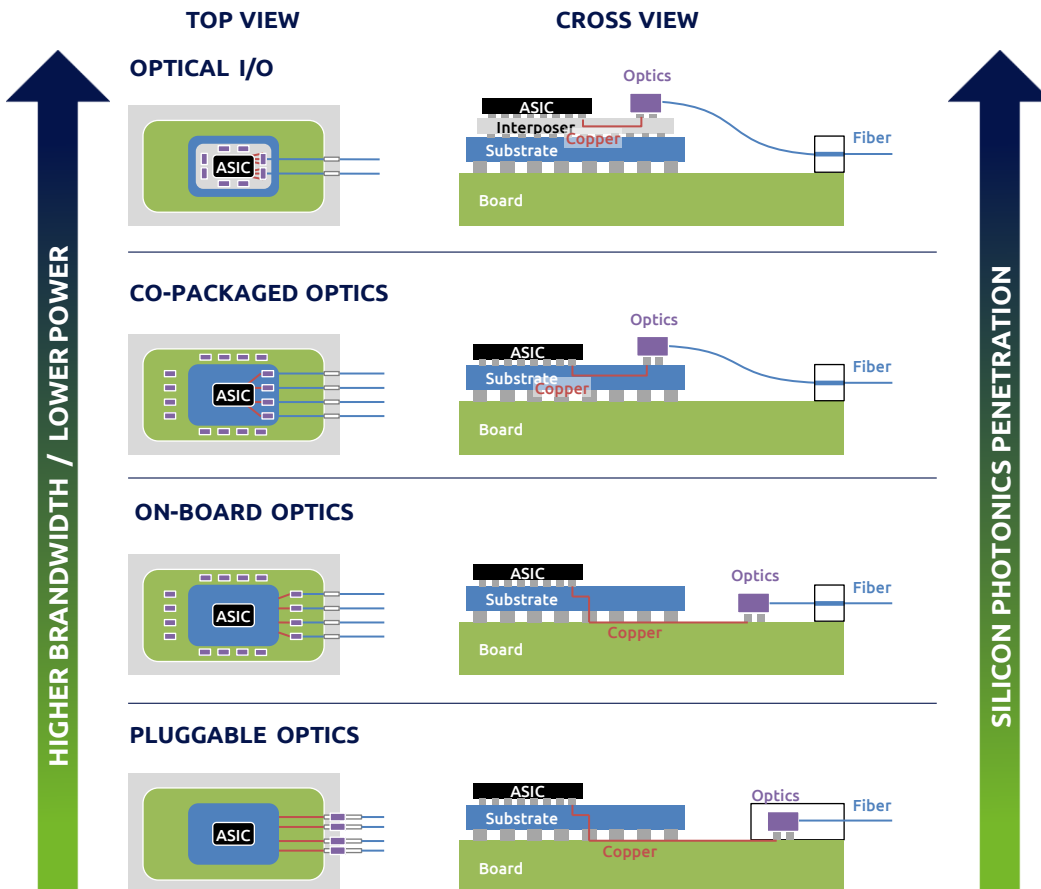


Source: ResearchandMarkets



OPTICAL I/O FOR GPU INTERCONNECTS

LOWER LATENCY AND 30% REDUCTION IN ENERGY CONSUMPTION



EMPOWERING AI ACCELERATION WITH OPTICAL I/O GPU INTERCONNECTS



ENHANCED BANDWIDTH

Enable improved bandwidth capabilities, for rapid and efficient data transfer between CPUs and GPUs in AI workloads



IMPROVED ENERGY CONSUMPTION

Enhance faster data transfer with lower power consumption, allowing for more computational work with reduced energy usage



REAL-TIME PROCESSING

Ensuring swift communication between the CPU and GPU, crucial for real-time AI processing



SCALABILITY

Allowing systems to easily accommodate increased computational demands in AI



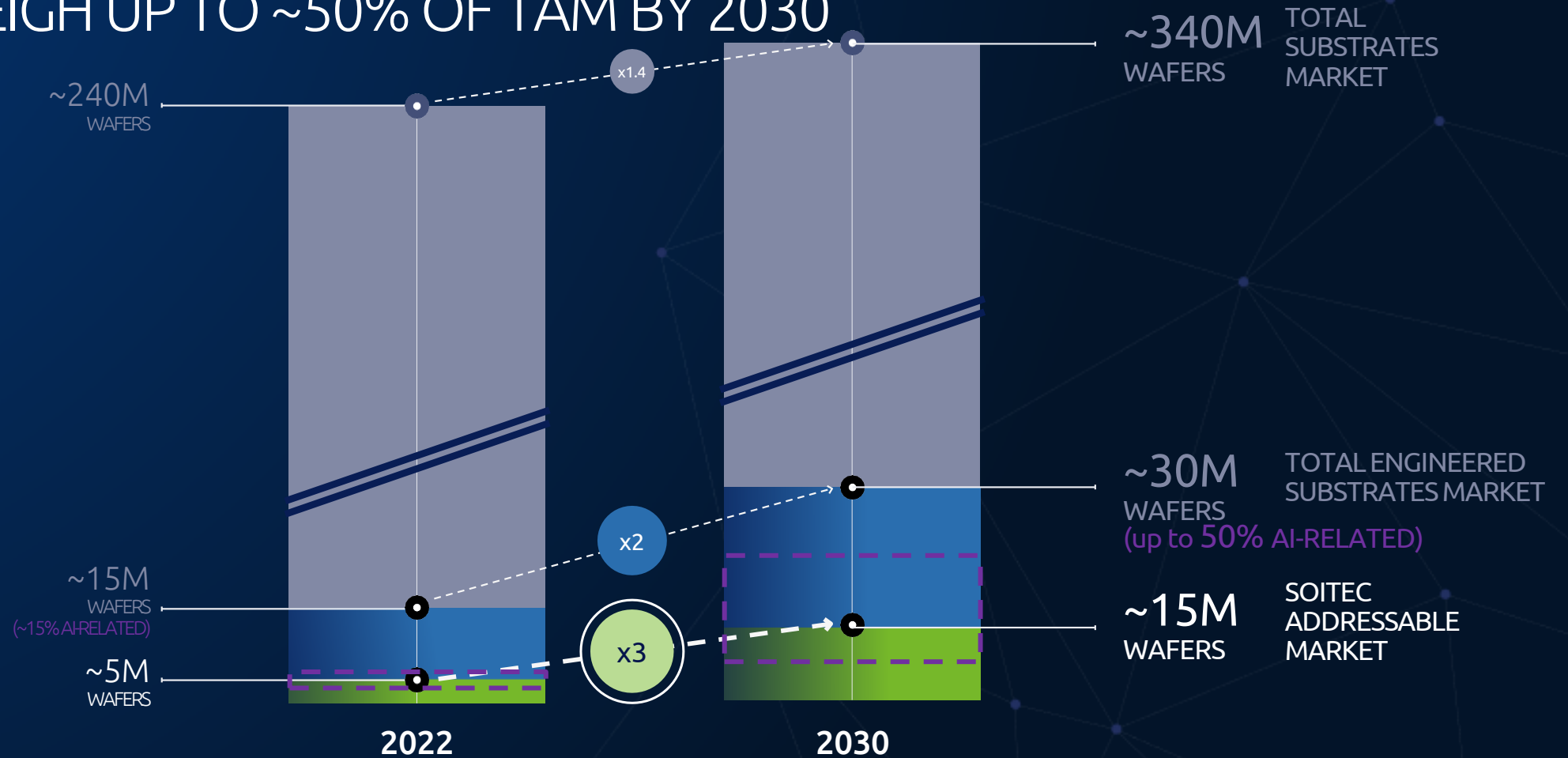
OPTIMIZED PARALLELISM

Essential in scenarios where multiple AI models or different types of computations run concurrently

ENGINEERED SUBSTRATES TO ENABLE AI ADOPTION

SOITEC ADDRESSABLE MARKET TO TRIPLE BY 2030

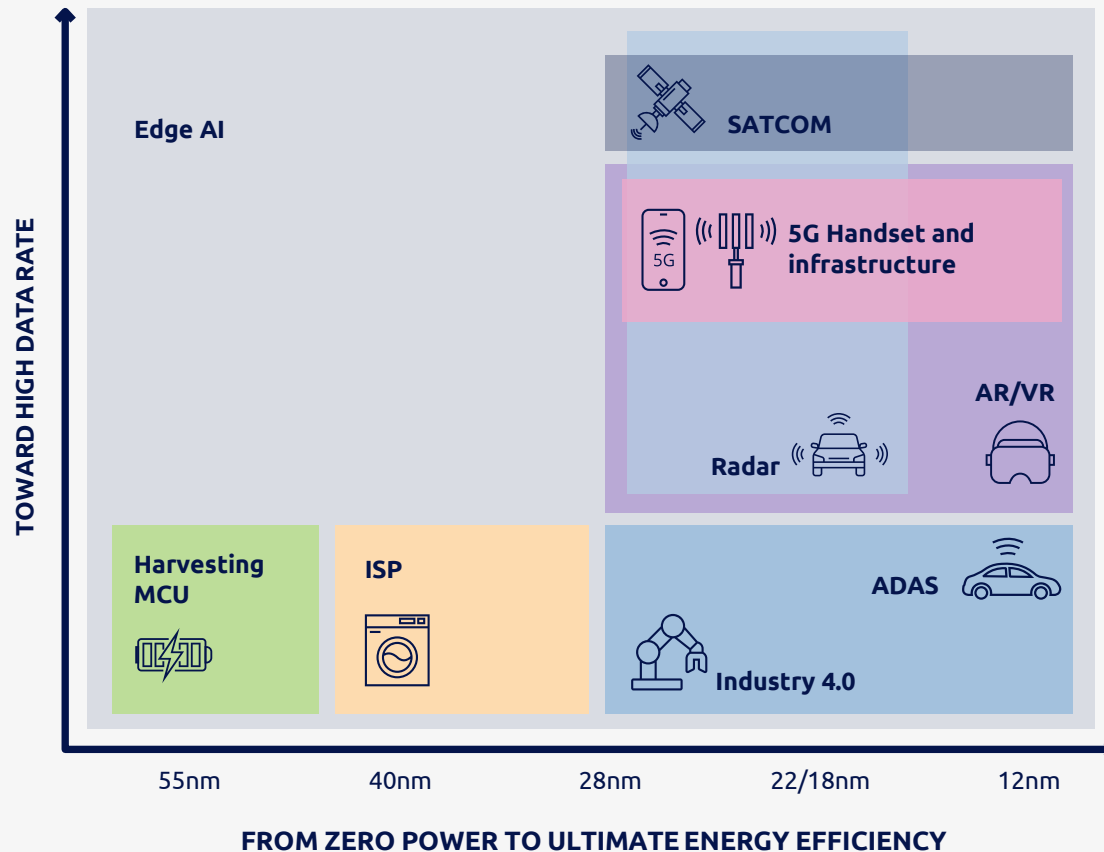
AI TO WEIGH UP TO ~50% OF TAM BY 2030



Source: Yole, SEMI, Soitec estimates
Box sizes for illustration purposes

FD-SOI

MARKET SEGMENTATION



FD-SOI IS THE ANSWER FOR APPLICATIONS REQUIRING

- Performance-on-demand
- Battery-powered
- Integrated RF
- Embedded NVM memories

3 MARKET DRIVERS

- AI MCUs
- 5G
- Automotive

FD-SOI Edge AI inference hardware will support all those segments



AUTOMOTIVE & INDUSTRIAL PRODUCT PORTFOLIO

AUTO FD-SOI



AUTO FD-SOI EMPOWERS THE FUTURE OF AUTOMOTIVE AND INDUSTRIAL SMART DEVICES



ADAS



Radars



Industrial Automation

AUTO FD-SOI ENABLES SUPERIOR PERFORMANCE OVER BULK SILICON AND FINFET

SAVING POWER

~30%

GREENHOUSE GASES
EMISSION REDUCTION

SAVING LIVES

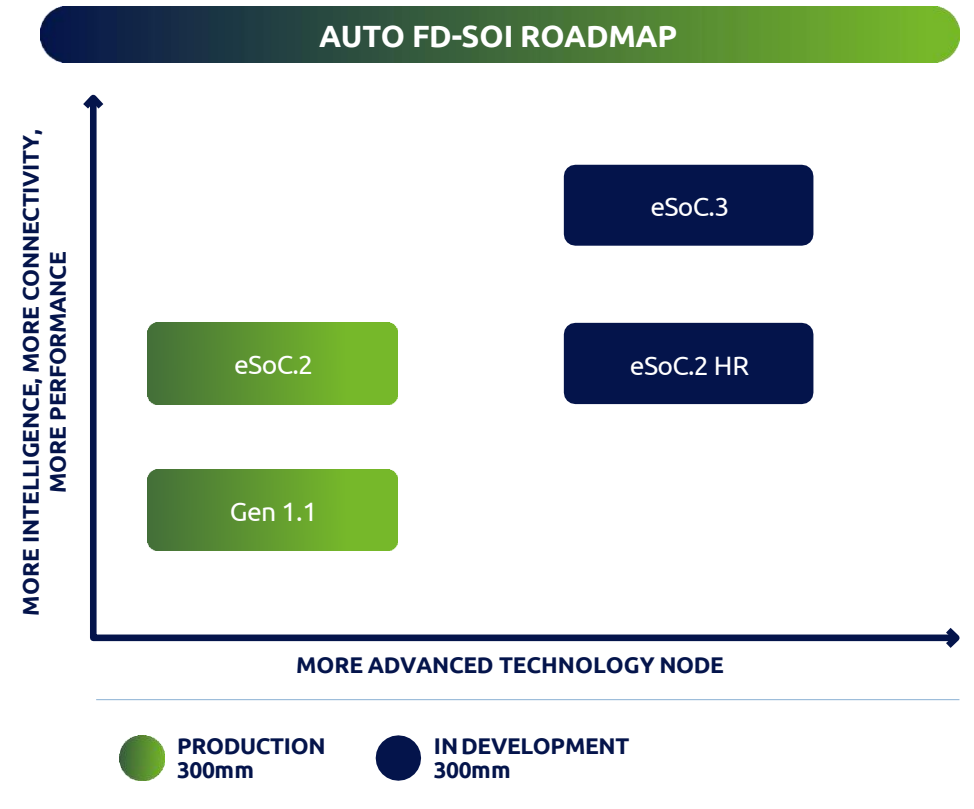
~50%

HIGHER DETECTION
RANGE IN RADARS

SAVING COST

~50%

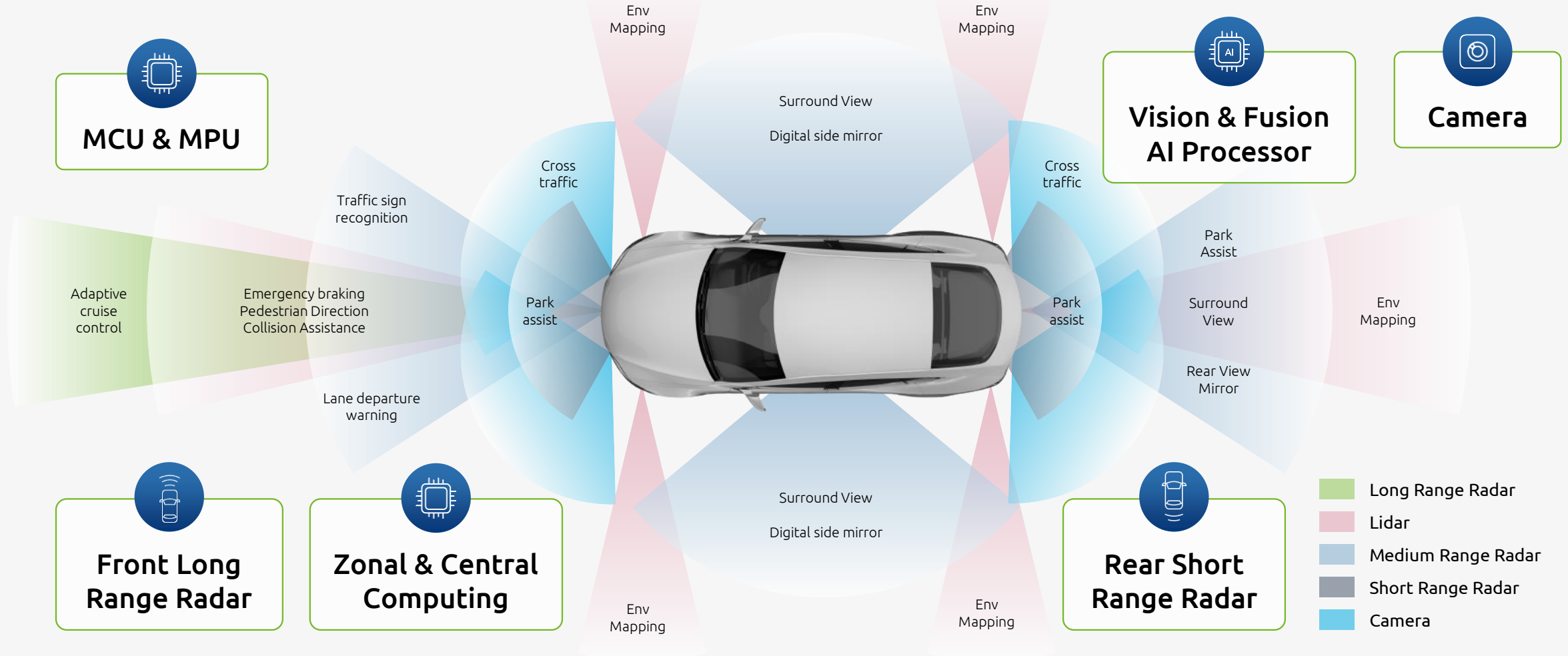
DIE SIZE
REDUCTION





AUTO FD-SOI FIELDS OF USE

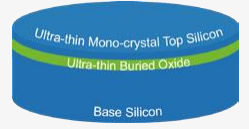
ACCELERATING VEHICLE AUTONOMY, DIGITISATION AND CONNECTIVITY





SMART DEVICES PRODUCT PORTFOLIO

SMART FD-SOI



SMART FD-SOI BENEFITS ALL PORTABLE APPLICATIONS



Smart Home Devices



Wearables



Environmental
Smart Sensors



Medical IoT

OUR SMART FD-SOI SUBSTRATE ENABLES



LOWER ACTIVE
POWER
CONSUMPTION -
ALWAYS ON



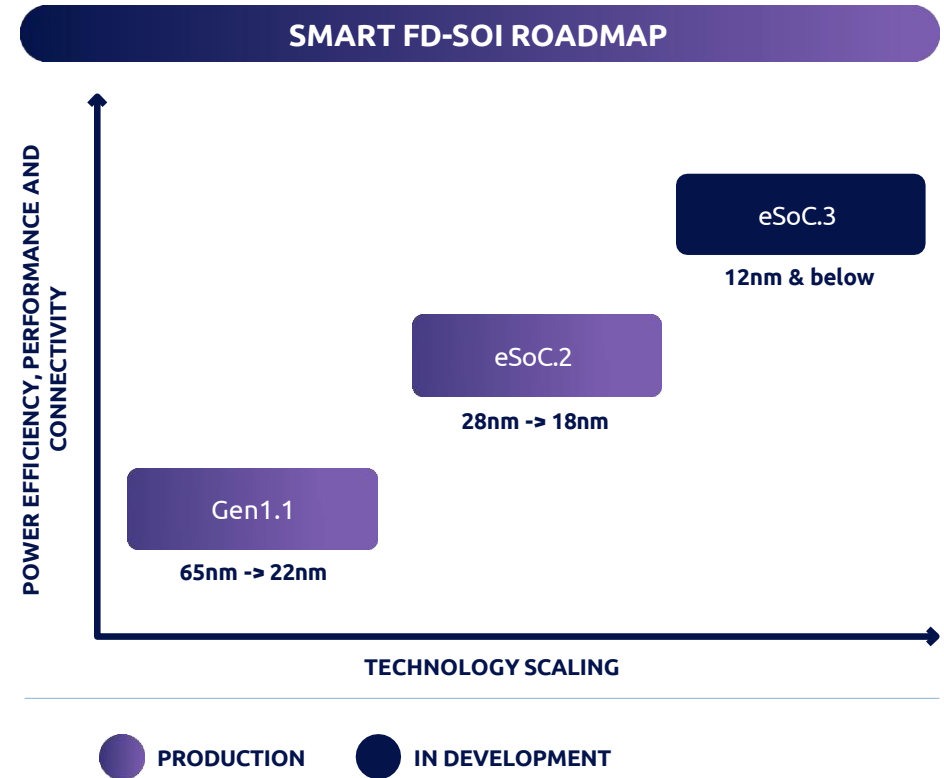
PERFORMANCE
ON DEMAND

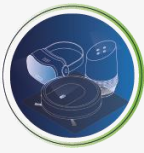


ROBUST ENERGY
HARVESTING
'ZERO POWER'
CAPABILITIES



LOWEST-COST
PROCESSING
(INFERENCES-PER-
WATT-PER-\$)





SMART DEVICES PRODUCT PORTFOLIO

SMART FD-SOI – LATTICE NEXUS FPGA PLATFORM FOR EDGE AI

LATTICE FPGA NEXUS PLATFORM



LOWER POWER
CONSUMPTION



FASTER
PERFORMANCE




SMALLER
SIZE



EMBEDDED NVM
MEMORIES

FD-SOI DELIVERING HIGHEST ENERGY SAVINGS IN BENCHMARK TESTS

	Operating Frequency		
	0 MHz	1 MHz	200 MHz
28nm FD-SOI 	34.6 mW (1x)	36.2 mW (1x)	248.6 mW (1x)
7nm FinFET	358 mW (10.0x)	360 mW (9.8x)	680 mW (2.7x)
10nm LP	244 mW (7.0x)	247 mW (6.8x)	880 mW (3.5x)

Built on FD-SOI, Lattice Nexus delivers up to **75% LOWER POWER** vs similar FPGAs and small form factor packaging with sizes as small as **4x4 mm**, perfect for many edge AI applications



Drones



AR/VR



Smart Speaker



Video
Surveillance



Wearables



Smart
Appliances



Consumer
Robots



Smart
Doorbell



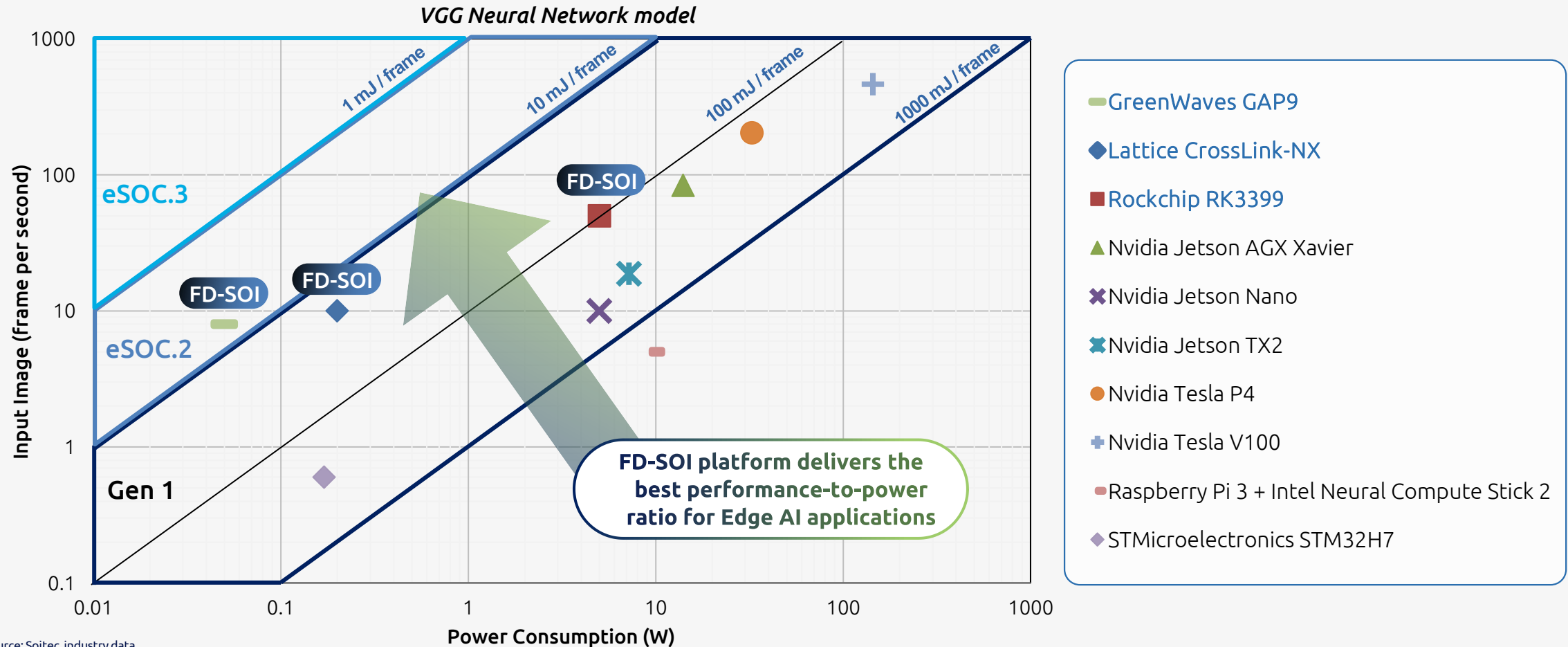
Smart
Toys

Source: Lattice



EDGE COMPUTING

FD-SOI IS THE IDEAL PLATFORM FOR EDGE AI

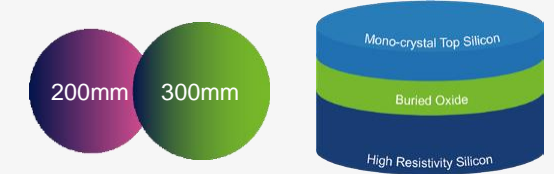


Source: Soitec, industry data

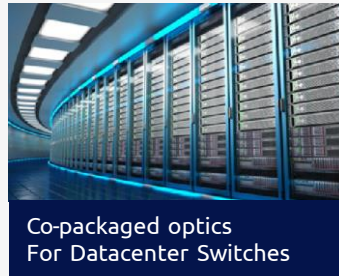
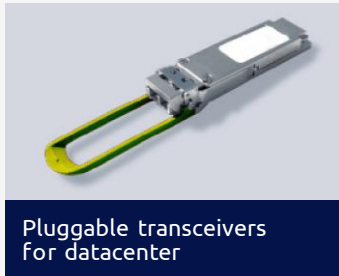


SMART DEVICES PRODUCT PORTFOLIO

SMART PHOTONICS-SOI



SMART PHOTONICS-SOI IS TARGETING ENERGY-EFFICIENT FAST DATA TRANSFER



OUR SMART PHOTONICS-SOI SUBSTRATE ENABLES



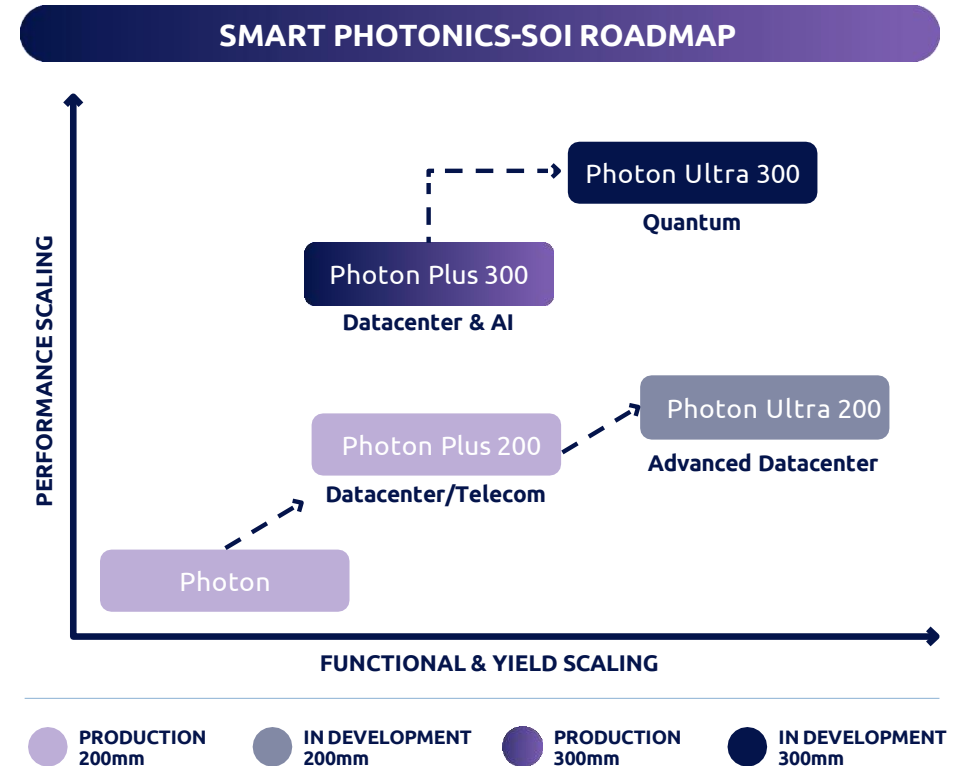
FASTER DATA
TRANSFER RATE



LOWER POWER
CONSUMPTION



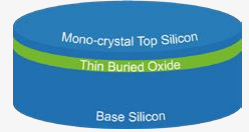
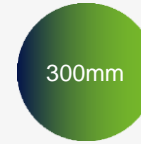
SIMPLER
PACKAGING





SMART DEVICES PRODUCT PORTFOLIO

SMART IMAGER-SOI



SMART IMAGER-SOI TARGETS 3D STACKING IMAGERS



Secure 3D Facial Recognition



Advanced Embedded Image processing

OUR SMART IMAGER-SOI SUBSTRATE ENABLES



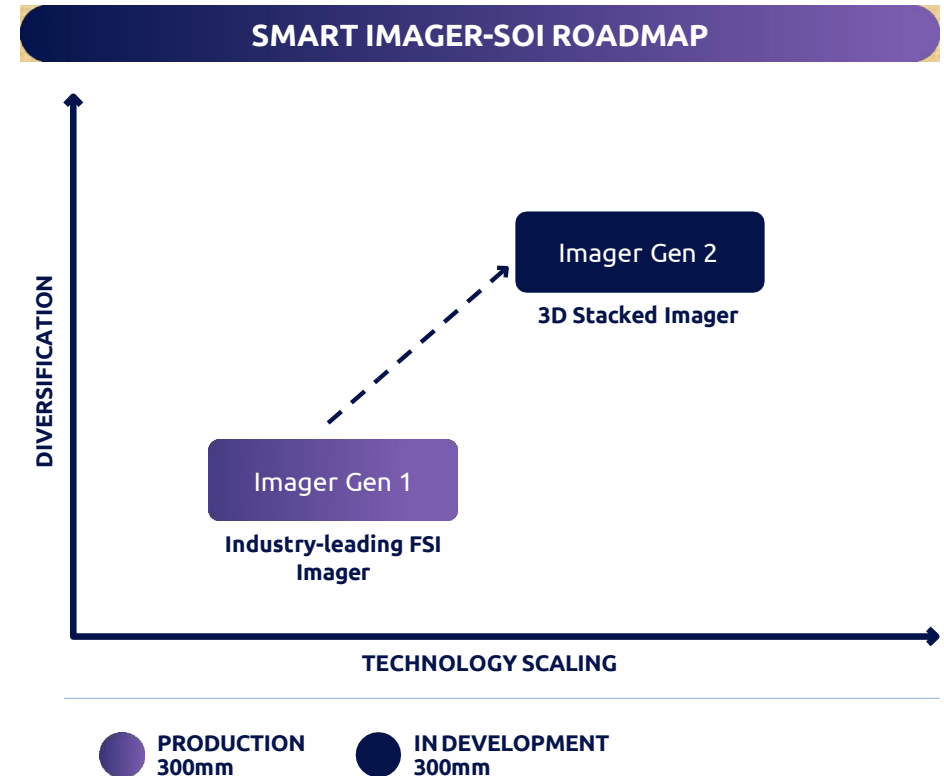
HIGHER RESOLUTION
FOR SECURITY
APPLICATION



INCREASE
ON-CHIP IMAGE
PROCESSING



IMPROVE
DETECTION EFFICIENCY
AND REDUCE POWER



AI-NABLERS

EXPANDING PRODUCT PORTFOLIO TO ENABLE FUTURE AI DEVELOPMENTS

		ACTIVE LAYER							
		Silicon	Piezo	SiC	InP	GaN	GaAs	Ge	Others
SUBSTRATE	Silicon or SOI								<div>Diamond GaOx</div> <div>2D materials</div>
	Sapphire								
	SiC or polySiC								
	GaAs								
	Device wafer								

AI-NABLERS

EXPANDING PRODUCT PORTFOLIO TO ENABLE FUTURE AI DEVELOPMENTS

AI-NABLER

		ACTIVE LAYER							
		Silicon	Piezo	SiC	InP	GaN	GaAs	Ge	Others
SUBSTRATE	Silicon or SOI	Low power Transistor isolation Radiation hardness Edge AI, 4G/5G, Datacenter, Imager	High performance RF filters 4G/5G	Co-integration Quantum	Co-integration Scalable to 300mm High performance 6G, SWIR, Imager	PA performance Co-integration 5G/6G, smartphones	Co-integration Scalable to 300mm Optoelectronics	Co-integration Scalable to 300mm High mobility Optoelectronics	Better performance Power electronics Better performance HPC, IoT
	Sapphire	Transistor isolation Radiation hardness 3G/4G			Co-integration High performance Optoelectronics	High performance microLEDs	Co-integration High performance Optoelectronics	Co-integration High performance Optoelectronics	
	SiC or polySiC			Better performance Higher yield Greener technologies Power electronics		PA performance Co-integration 5G/6G, baseband			
	GaAs				Optical performance Optoelectronics		Optical performance Optoelectronics		
	Device wafer	Uniformity Crystal quality SoC integration 3D Sequential integration	Uniformity Crystal quality Yield Sensors, Actuators						





ENABLING ARTIFICIAL INTELLIGENCE
WITH ENGINEERED SUBSTRATES

THANK YOU